The SM2270 SSD controller enables high performance and high capacity SSD solutions with comprehensive firmware. It offers a combination of high performance, high reliability, and flexible turnkey standard NVMe and Open Channel SSD firmware ideal for use in data center applications.

The SM2270 is a high performance, 16 channel enterprise NVMe controller featuring a PCIe Gen3 x8 host interface, dual 32 bit DRAM data bus, and a NVMe management interface through the SMBus for out-of-band management. The controller’s triple ARM Cortex R5 Dual-core CPUs provide guaranteed latency and outstanding performance of 4KB random read up to 800,000 IOPS, and 4KB random write up to 200,000 IOPS.

The SM2270 ensures reliable operation and long lifespan of SSDs using the latest 3D NAND and QLC NAND Flash technology. Data integrity is safeguarded by end-to-end data path protection and Error Correction Code (ECC). Silicon Motion’s 6th generation proprietary NANDXtend technology integrating error recovery with a machine learning algorithm prolongs data retention even when operating at high temperature. It also enhances data reliability throughout the SSD’s lifespan for data integrity and QoS. Furthermore, Power Loss Protection can prevent data loss in the event of unexpected power outages.

The SM2270 thus provides a complete controller solution for SSDs which supports the latest technology from major NAND Flash manufacturers, provides flexible firmware design to meet the demanding requirements of data center applications, and offers reliable data storage over a long operating lifespan.

**KEY FEATURES**

- **High Performance**
  - High and consistent random IOPS
  - Low latency with ensured QoS

- **Superior Data Integrity**
  - End-to-end data protection
  - ECC on DRAM and SRAM

- **Flexible Application Implementation**
  - Standard NVMe turnkey FW
  - Customized FW supporting KV and Open Channel SSD

- **Long SSD Lifespan**
  - LDPC engine for superior error correction capability
  - Programmable RAID
## SPECIFICATIONS

### SM2270

<table>
<thead>
<tr>
<th>Host interface</th>
<th>PCIe Gen3 x8</th>
</tr>
</thead>
<tbody>
<tr>
<td>Command Protocol</td>
<td>NVMe 1.3</td>
</tr>
<tr>
<td>Processor</td>
<td>Triple ARM Cortex R5 Dual-core CPUs</td>
</tr>
</tbody>
</table>
| Flash Controller | - 16CH/8CE for total 128CE  
                  | - 3D TLC/QLC              
                  | - Up to 16TB              |
| DRAM Controller  | - Dual, 32-bit data bus width  
                  | - DDR3/3L/4-2133 and LPDDR3-1600  
                  | - Max addressing to 16GB with banking |
| Data Integrity and Reliability | - 2KB LDPC  
                                | - Programmable RAID         
                                | - E2E DPP (512B+2B-CRC)     
                                | - DRAM ECC (SECDED/32B+2B)  
                                | - SRAM ECC (SECDED/32b+7b)  |
| Performance (U.2) | - Seq. Read: up to 3,200 MB/s  
                       | - Seq. Write: up to 2,800 MB/s  
                       | - 4KB Random Read: up to 800K IOPS  
                       | - 4KB Random Write: up to 200K IOPS |
| Temperature       | - 0 ~ 70°C (Commercial)  
                  | - -40 ~ 85°C (Industrial)  |
| Package           | 961-ball FCBGA (21mmx21mm) |

### Enabling Data Center Optimized SSDs

- **User SW Engine**
  - STD NVMe Driver LBA
  - User Space File System
  - Block I/O
- **NVMe SSD**
  - Enterprise Turnkey NVMe FW; FTL Management
  - Standard Turnkey FW Stack

- **User SW Engine**
  - User Space File System
  - Kernel-Space FTL / SSD Feature Driver PPA
  - Block I/O
- **Open Channel SSD**
  - OCS Media Interface FW; v1.2,v2.0, Denali
  - OCS Software Package